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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/681,517	10/07/2003	Chung When Lo	Ralink-12US	9635
75	590 04/05/2005		EXAMINER	
Maryam Imam Esq. IMAM & ASSOCIATES Suite 1010 111 North Market Street San Jose, CA 95113			NGUYEN, HAI L	
			ART UNIT	PAPER NUMBER
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			2816	
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Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
	10/681,517	LO ET AL.	\mathcal{G} \mathcal{M}
Office Action Summary	Examiner	Art Unit	
	Hai L. Nguyen	2816	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet	with the correspondence ac	ddress
A SHORTENED STATUTORY PERIOD FOR RETHE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFI after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, and the sum of the second	N. R 1.136(a). In no event, however, may a reply within the statutory minimum of the riod will apply and will expire SIX (6) MC ratute, cause the application to become a	a reply be timely filed sirty (30) days will be considered time DNTHS from the mailing date of this of ABANDONED (35 U.S.C. § 133).	ly. communication.
Status			
 1) ⊠ Responsive to communication(s) filed on 0 2a) ☐ This action is FINAL. 2b) ☑ 1 3) ☐ Since this application is in condition for allo closed in accordance with the practice und 	This action is non-final. wance except for formal ma	• •	e merits is
Disposition of Claims			
4) Claim(s) 1-20 is/are pending in the applicate 4a) Of the above claim(s) is/are with 5) Claim(s) is/are allowed. 6) Claim(s) 1-20 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) is/are objected to restriction and Application Papers 9) The specification is objected to by the Example 100 The decirios (s) find an 08 December 2003	drawn from consideration. nd/or election requirement. niner.	∴ chicated to by the Even	
10) ☐ The drawing(s) filed on <u>08 December 2003</u> Applicant may not request that any objection to Replacement drawing sheet(s) including the color of the oath or declaration is objected to by the	the drawing(s) be held in abeya	ance. See 37 CFR 1.85(a). g(s) is objected to. See 37 C	FR 1.121(d).
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for force a) All b) Some * c) None of: 1. Certified copies of the priority document of: 2. Certified copies of the priority document	nents have been received. Items have been received in priority documents have been reau (PCT Rule 17.2(a)).	Application No n received in this National	Stage
Attachment(s) Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB Paper No(s)/Mail Date	Paper No	Summary (PTO-413) o(s)/Mail Date Informal Patent Application (PTo	O-152)

DETAILED ACTION

Specification

1. The disclosure is objected to because of the following informalities: page 2, line 11, "16" should be changed to --17-- as shown in FIG. 1.

Appropriate correction is required.

2. Claims 14 and 16 are objected to under 37 CFR 1.75(c), as being of improper dependent form for failing to further limit the subject matter of a previous claim. Applicant is required to cancel the claim(s), or amend the claim(s) to place the claim(s) in proper dependent form, or rewrite the claim(s) in independent form. A receiver limitation of claim 14 does not further define the phase control loop circuit of claim 1.

Claim Rejections - 35 USC § 112

- 3. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 4. Claims 1-18 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 1 is indefinite because the limitations "a reference frequency signal", on line 3, and "a reference frequency", on line 20, lack clear antecedent basis. It appears that this "reference frequency signal" is the same as "reference frequency signal" recited on line 1 of claim 1; and this "reference frequency" is the same as "reference frequency" recited on line 4 of claim 1.

Claims 2-18 are rejected due to their dependencies on claim 1.

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Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 6. Claims 1-5 and 17-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Bellaouar (US 6,566,966).

With regard to claims 1 and 19, Bellaouar discloses in Fig. 1 a phase control loop circuit, and a method of use thereof, for tuning to a reference frequency signal comprising a phase lock loop (PLL) circuit (10) being responsive to the reference frequency signal (Fref), the PLL circuit including a voltage control oscillator (VCO; 14) for generating a VCO output (Fin); the phase control loop circuit processing the VCO output to generate an output frequency signal (Fv); and a coarse tuning circuit (18- 24) being responsive to the PLL output (Fin), for processing the same to generate a counter output (18), the VCO being responsive to the counter output, the counter output being used for coarse tuning the output frequency signal to the reference frequency signal, the coarse tuning circuit further responsive to a lock detection signal (START), the lock detection signal for controlling the counter output to cause the output frequency to be within a predetermined range of frequencies including the reference frequency, the PLL circuit for fine tuning the output frequency signal to the reference frequency signal, wherein the PLL circuit and the coarse tuning circuit tune the output frequency to a reference frequency included in a wide range of frequencies.

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With regard to claims 2-5 and 17-18, the references also meet the recited limitations in these claims.

Claim 20 is similar rejected; note the above discussion with regard to claims 1 and 19.

Allowable Subject Matter

7. Claims 6-13 and 15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art of record fails to disclose or fairly suggest a phase control loop circuit (30 in instant Fig. 3) for tuning to a reference frequency signal (70), as recited in claim 6, having specific structural limitations such as a coarse tuning circuit (44) includes a comparator circuit (46), the comparator circuit including a first comparator (48) and a second comparator (50), the Vctrl signal (49) being included in the PLL output, the first and second comparators being responsive to the PLL output, the first comparator being responsive to a first fixed value signal having a first voltage value (Vcc*2/3), the second comparator being responsive to a second fixed value signal having a second voltage value(Vcc*2/3); and being configured in combination with the rest of the limitations of the base claims and any intervening claims.

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Sutton (US 6,380,810) is cited as of interest because it discloses a reduced lock time for a phase locked loop circuit.

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9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hai L. Nguyen whose telephone number is 571-272-1747 and

Right Fax number is 571-273-1747. The examiner can normally be reached on Monday-

Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Timothy Callahan can be reached on 571-272-1740. The official fax phone number

for the organization where this application or proceeding is 703-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is 571-272-1562.

10. Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private

PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).